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~ FILE 'USPAT' ENTERED AT 09:10:05 ON 21 MAR 95)  
L1 161867 S AMPLIFIER?  
L2 5001 S CURRENT MIRROR?  
L3 3963 S L1 AND L2  
L4 13913 S PNP AND NPN  
L5 1403 S L3 AND L4  
L6 24589 S SOURC? AND SINK?  
L7 335 S L5 AND L6  
L8 228978 S CLAMP?  
L9 95 S L7 AND L8  
L10 34 S L9 AND 327/CLAS  
L11 32 S L9 AND 330/CLAS  
L12 19 S L9 AND 323/CLAS  
L13 42 S L9 AND BALANC?  
=> d l13 1-42

1. 5,367,247, Nov. 22, 1994, Critically continuous boost converter; William S. Blocher, et al., 323/222, 284, 288 [IMAGE AVAILABLE]
2. 5,367,211, Nov. 22, 1994, Differential \*\*amplifier\*\* with hysteresis; Raymond L. Giordano, et al., 327/73, 52, 205; 330/257 [IMAGE AVAILABLE]
3. 5,334,928, Aug. 2, 1994, Frequency compensation circuit for low dropout regulators; Robert C. Dobkin, et al., 323/280, 273 [IMAGE AVAILABLE]
4. 5,274,323, Dec. 28, 1993, Control circuit for low dropout regulator; Robert C. Dobkin, et al., 323/280, 273, 281 [IMAGE AVAILABLE]
5. 5,264,782, Nov. 23, 1993, Dropout recovery circuit; Stephen F. Newton, 323/288, 222, 901; 363/49 [IMAGE AVAILABLE]
6. 5,264,780, Nov. 23, 1993, On time control and gain circuit; Richard E. Bruer, et al., 323/222, 284, 288 [IMAGE AVAILABLE]
7. 5,180,932, Jan. 19, 1993, Current mode multiplexed sample and hold circuit; David W. Bengel, 327/362, 415 [IMAGE AVAILABLE]
8. 5,153,529, Oct. 6, 1992, Rail-to-rail input stage of an operational \*\*amplifier\*\*; Rikki Koda, et al., 330/295, 257, 261, 311 [IMAGE AVAILABLE]
9. 5,140,280, Aug. 18, 1992, Rail-to-rail output stage of an operational \*\*amplifier\*\*; Robert L. Vyne, et al., 330/255, 267 [IMAGE AVAILABLE]
10. 4,970,452, Nov. 13, 1990, Integrated circuit comprising a switchable current generator; Stephane Barbu, et al., 323/317, 315; 330/257, 288 [IMAGE AVAILABLE]
11. 4,934,770, Jun. 19, 1990, Electronic compression system; James R. Anderson, et al., 330/278, 279 [IMAGE AVAILABLE]
12. 4,926,139, May 15, 1990, Electronic frequency filter; James R. Anderson, et al., 330/294; 327/552; 330/107, 306 [IMAGE AVAILABLE]
13. 4,922,131, May 1, 1990, Differential voltage threshold detector; James R. Anderson, et al., 327/66, 53 [IMAGE AVAILABLE]

14. 4,908,527, Mar. 13, 1990, Hall-type transducing device; Joel C. Van Antwerp, 327/511; 257/423; 324/235, 251; 327/513; 338/32H [IMAGE AVAILABLE]
15. 4,882,762, Nov. 21, 1989, Multi-band programmable compression system; Fred D. Waldhauer, 381/106; 455/72 [IMAGE AVAILABLE]
16. 4,882,761, Nov. 21, 1989, Low voltage programmable compressor; Fred D. Waldhauer, 381/106; 455/72 [IMAGE AVAILABLE]
17. 4,868,517, Sep. 19, 1989, Variolosser; Fred D. Waldhauer, et al., 330/254, 285 [IMAGE AVAILABLE]
18. 4,792,977, Dec. 20, 1988, Hearing aid circuit; James R. Anderson, et al., 381/68.4, 68, 68.2, 104 [IMAGE AVAILABLE]
19. 4,783,637, Nov. 8, 1988, Front end stage of an operational \*\*amplifier\*\*; Gerald M. Cotreau, 330/295, 255, 257, 261 [IMAGE AVAILABLE]
20. 4,775,843, Oct. 4, 1988, Wideband post \*\*amplifier\*\* for product detector; Milton E. Wilcox, 330/260, 293 [IMAGE AVAILABLE]
21. 4,742,309, May 3, 1988, Line receiver; John W. Chu, 330/258, 257; 375/36 [IMAGE AVAILABLE]
22. 4,636,744, Jan. 13, 1987, Front end of an operational \*\*amplifier\*\*; Ken R. King, et al., 330/295, 255, 257, 261 [IMAGE AVAILABLE]
23. 4,636,743, Jan. 13, 1987, Front end stage of an operational \*\*amplifier\*\*; Gerald M. Cotreau, 330/295, 255, 257, 261 [IMAGE AVAILABLE]
24. 4,499,408, Feb. 12, 1985, Control circuit for an electronically commutated motor, an integrated circuit for an ECM, and a method of operating an ECM; Ricky F. Bitting, et al., 318/254, 138, 439 [IMAGE AVAILABLE]
25. 4,492,824, Jan. 8, 1985, Speakerphone linear comparator; Steve W. Bell, et al., 379/389; 327/66; 330/69, 252, 288 [IMAGE AVAILABLE]
26. 4,491,772, Jan. 1, 1985, Control circuit for an electronically commutated motor (ECM), method of timing the electronic commutation of an ECM, and method of operating an ECM; Ricky F. Bitting, 318/254, 138, 439 [IMAGE AVAILABLE]
27. 4,468,652, Aug. 28, 1984, Digital-to-analog converter having ladder network and improved interconnection therefor; Anthony D. Wang, et al., 341/118, 154 [IMAGE AVAILABLE]
28. 4,423,409, Dec. 27, 1983, Digital-to-analog converter having single-ended input interface circuit; Jimmy R. Naylor, et al., 341/133; 327/481, 584; 341/118 [IMAGE AVAILABLE]
29. 4,381,497, Apr. 26, 1983, Digital-to-analog converter having open-loop voltage reference for regulating bit switch currents; William J. Lillis, et al., 341/119; 323/315; 341/135, 153 [IMAGE AVAILABLE]

30. 4,378,529, Mar. 29, 1983, Differential \*\*amplifier\*\* input stage capable of operating in excess of power supply voltage; Robert C. Dobkin, 330/257, 258, 259 [IMAGE AVAILABLE]
31. 4,295,101, Oct. 13, 1981, Class AB push-pull quasi-linear \*\*amplifiers\*\*; Arthur J. Leidich, 330/262, 273 [IMAGE AVAILABLE]
32. 4,292,478, Sep. 29, 1981, Interface circuits; John C. H. Davis, et al., 379/405; 327/563 [IMAGE AVAILABLE]
33. 4,266,125, May 5, 1981, Optical shaft angle encoder; Howard C. Epstein, et al., 250/231.16, 231.17, 237G [IMAGE AVAILABLE]
34. 4,263,506, Apr. 21, 1981, Pulse generating apparatus; Howard C. Epstein, 250/231.16, 231.17, 237G [IMAGE AVAILABLE]
35. 4,259,570, Mar. 31, 1981, Optical comparator; Mark Leonard, 250/214A, 231.16, 231.17 [IMAGE AVAILABLE]
36. 4,255,789, Mar. 10, 1981, Microprocessor-based electronic engine control system; Thomas W. Hartford, et al., 364/431.06; 123/417, 480, 492; 364/723 [IMAGE AVAILABLE]
37. 4,220,930, Sep. 2, 1980, Quasi-linear \*\*amplifier\*\* with feedback-controlled idling currents; Adel A. A. Ahmed, 330/270, 271, 273 [IMAGE AVAILABLE]
38. 4,215,282, Jul. 29, 1980, Temperature compensated sense \*\*amplifier\*\* for PROMs and the like; George W. Brown, et al., 327/82; 326/42; 327/306, 512 [IMAGE AVAILABLE]
39. 4,114,089, Sep. 12, 1978, Ground fault detecting apparatus including current-responsive threshold detection circuitry; Adel Abdel Aziz Ahmed, 324/509; 340/651; 361/44, 110 [IMAGE AVAILABLE]
40. 4,037,155, Jul. 19, 1977, Current-responsive threshold detection circuitry; Adel Abdel Aziz Ahmed, 324/133, 509; 327/50; 340/664; 361/44 [IMAGE AVAILABLE]
41. 3,967,207, Jun. 29, 1976, \*\*Amplifier\*\* with over-current protection; Carl Franklin Wheatley, Jr., 330/309, 207P, 298; 361/88, 98 [IMAGE AVAILABLE]
42. 3,953,767, Apr. 27, 1976, Ground fault detection apparatus; Adel Abdel Aziz Ahmed, 361/44 [IMAGE AVAILABLE]

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      (FILE 'USPAT' ENTERED AT 09:39:48 ON 21 MAR 95)
L1      5001 S CURRENT MIRROR?
L2      13913 S PNP AND NPN
L3      1722 S L1 AND L2
L4      161867 S AMPLIFIER?
L5      1403 S L3 AND L4
L6      47601 S SINK?
L7      338 S L5 AND L6
L8      228978 S CLAMP?
L9      96 S L7 AND L8
L10     206307 S BALANC?
L11     42 S L9 AND L10
L12     6807 S DARLINGTON
L13     8 S L11 AND L12
=> d l13 1-8
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1. 4,908,527, Mar. 13, 1990, Hall-type transducing device; Joel C. Van Antwerp, 327/511; 257/423; 324/235, 251; 327/513; 338/32H [IMAGE AVAILABLE]

2. 4,742,309, May 3, 1988, Line receiver; John W. Chu, 330/258, 257; 375/36 [IMAGE AVAILABLE]

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6. 4,037,155, Jul. 19, 1977, Current-responsive threshold detection circuitry; Adel Abdel Aziz Ahmed, 324/133, 509; 327/50; 340/664; 361/44 [IMAGE AVAILABLE]

7. 3,967,207, Jun. 29, 1976, \*\*Amplifier\*\* with over-current protection; Carl Franklin Wheatley, Jr., 330/309, 207P, 298; 361/88, 98 [IMAGE AVAILABLE]

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